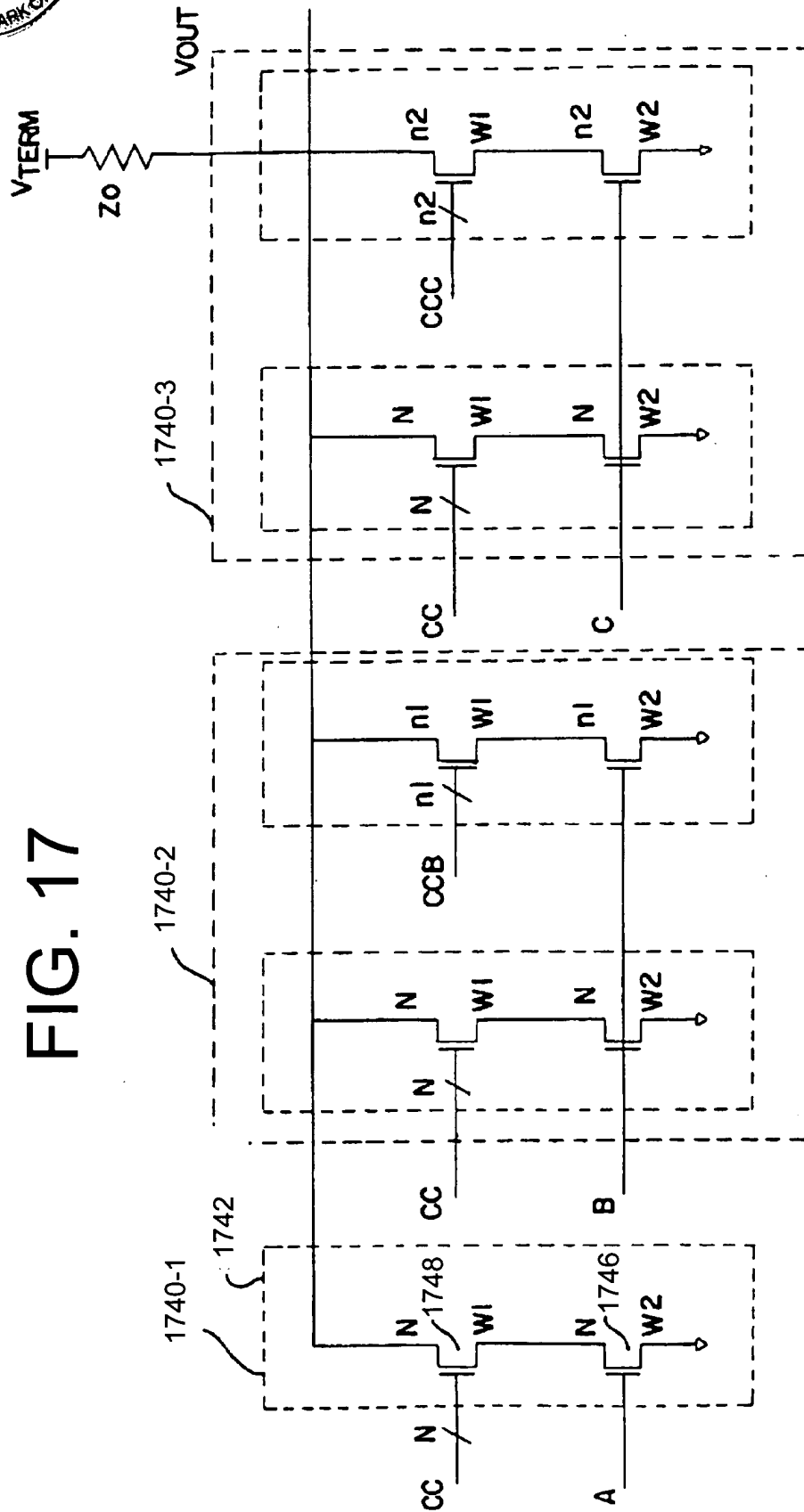
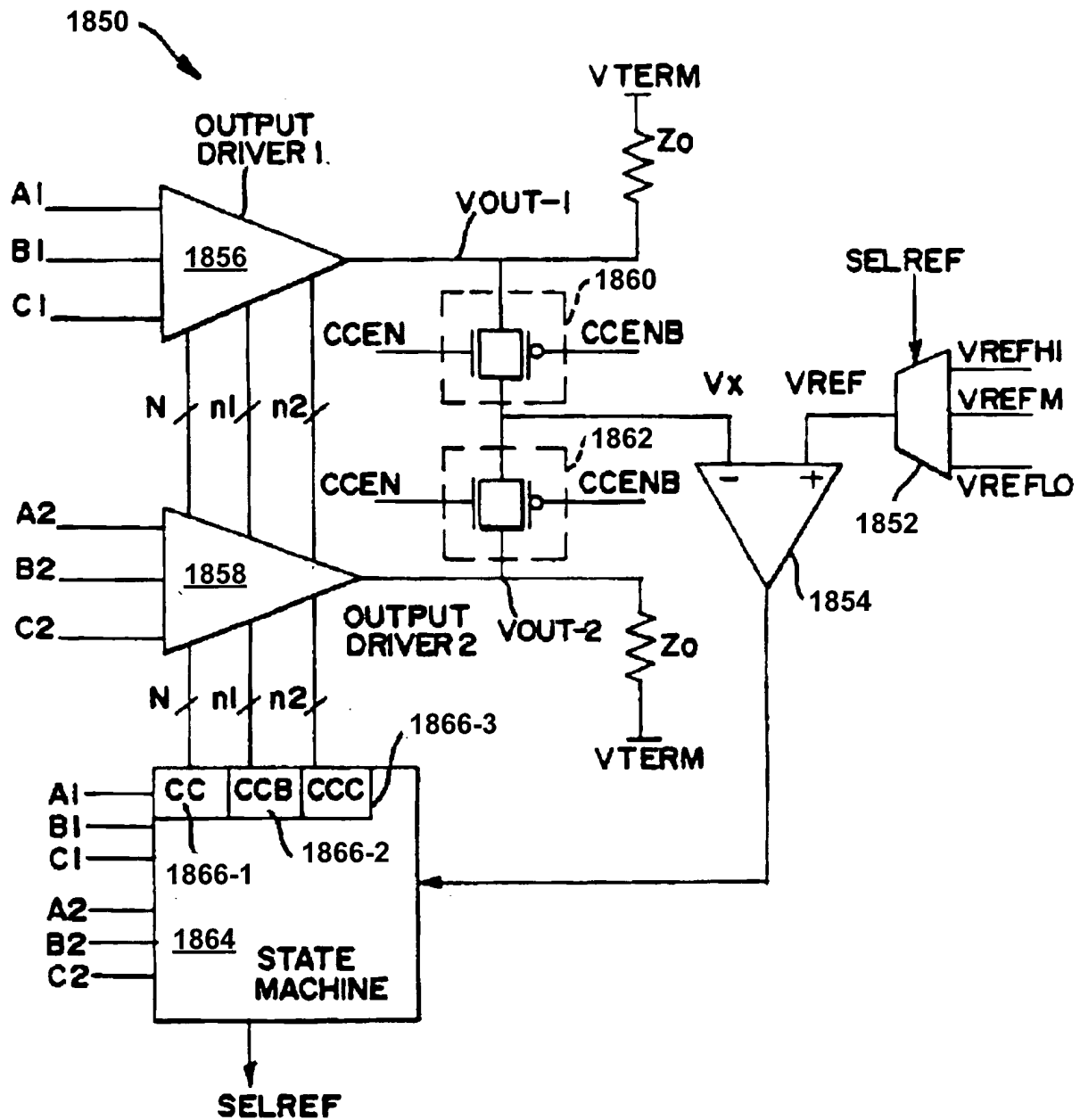


FIG. 17



GDS COMPENSATED MULTI-PAM OUTPUT DRIVER WITH CURRENT CONTROL

FIG. 18



CIRCUIT FOR CALIBRATING THE GDS COMPENSATED
OUTPUT DRIVER WITH CURRENT CONTROL

FIG. 19A

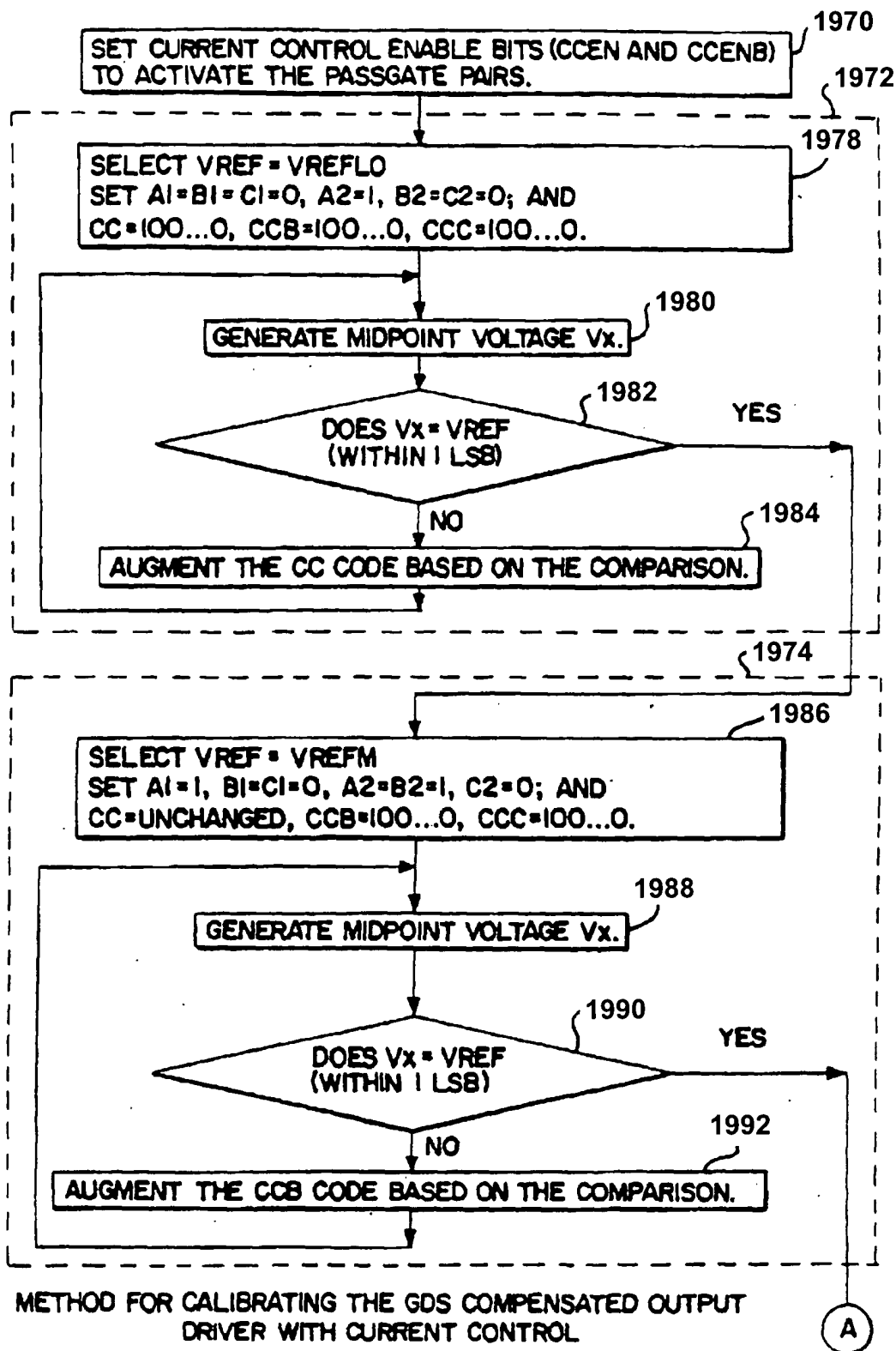


FIG. 19B

